

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

WARREN M. FARNWORTH  
SALMAN AKRAM

3 / IDS  
E. Hillis  
7-2-01

Serial No.:

ART UNIT: 2858

Filing Date: April 27, 2001

EXAMINER: KORBERT, R.

Division of Serial No. 09/266,237  
filed on March 10, 1999

For: TEST INTERCONNECT FOR BUMPED  
SEMICONDUCTOR COMPONENTS AND  
METHOD OF FABRICATION

Attorney Docket No. 97-1433.1

**INFORMATION DISCLOSURE STATEMENT**  
**April 27, 2001**

Assistant Commissioner for Patents  
BOX PATENT APPLICATION  
Washington, D.C. 20231

Sir:

The following sections are being submitted for this  
Information Disclosure Statement:

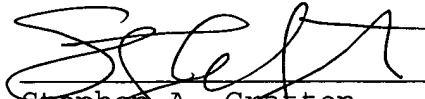
1.   x   Preliminary Statements.
2.   x   FORM PTO SB/08
3.        Statement As to Information Material to  
Examination Not Found in Patents or Publications.
4.   x   Identification of Prior Application in Which Listed  
Information Was Already Cited and For Which No Copies  
are Submitted Or Need be Submitted.
5.        Cumulative Patents or Publications.
6.        Copies of Listed Information Items Accompanying  
This Statement.
7.        Concise Explanation of Non-English Language  
Listed Information Items.
8.        Translation(s) of Non-English Language Documents.





DATED this 27th day of April, 2001.

Respectfully submitted:

  
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Stephen A. Gratton  
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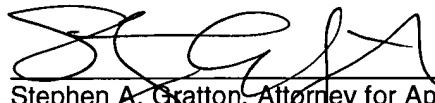
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April 27, 2001  
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Stephen A. Gratton, Attorney for Applicants